



GAU-1765#IDS  
Patent  
2-28-01

Docket No.: CDST-C130.CIP

# Inv ntion Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	2/16/01	Name of Person Making the Deposit:	Luz Costi/16
		Signature of the Person Making the Deposit:	<i>[Signature]</i>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Lee et al.

Serial No.: 09/588,115

Group Art Unit: 1765

Filed: May 30, 2000

Examiner: Not yet assigned

Title: MULTILAYER ELECTRODE STRUCURE AND METHOD FOR FORMING MULTILAYER ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY DEVICE

The Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

### Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- ..... Formal drawings, totaling ..... sheets.
- ..... Informal drawings, totaling ..... sheets.
- ☒ X Certification for PTO Consideration
- ☒ X Information Disclosure statement ( 1 sheets)
- ..... Information Disclosure statement and late filing fee
- ☒ X Form 1449
- ..... Petition for Extension of Time
- ..... Other: .....

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Fee Calculation (for other than a small entity)					
Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$ .00	0.00
Information Disclosure Statement, late filing				\$240.00	0.00
Other:					
Total Fees					0.00

## PAYMENT OF FEES

1. The full fee due in connection with this communication is provided as follows:
  - [ ] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.  
A duplicate copy of this authorization is enclosed.
  - [ ] A check in the amount of \$
  - [ X ] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.


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Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

Date: 2/16/01

By:   
John P. Wagner, Jr.  
Reg. No. 35,398



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: CDST-C130.CIP

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	1/26/01	Name of Person Making the Deposit:	Luz Castib
		Signature of the Person Making the Deposit:	[Signature]

Inventor(s): Lee et al.

Serial No.: 09/588,115

Group Art Unit: 1765

Filed: May 31, 2000

Examiner: Not yet assigned

Title: MULTILAYER ELECTRODE STRUCTURE AND METHOD FOR FORMING MULTILAYER ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY DEVICE

The Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Certification for PTO Consideration of an Information Disclosure Statement  
(Under 37 CFR §1.97)

Consideration of the enclosed Information Disclosure Statement is requested.

1. This certification is being made for this information disclosure statement

- ☒ [ X ] accompanying this certification  
☐ [ ] Filed \_\_\_\_\_

2. I hereby certify that:

- ☒ X Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.  
☐ No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, or, to the knowledge of the person signing certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement.

3. The person making this certification is

- ☒ [ x ] a person who is substantively involved in the preparation or prosecution of the application, and who is associated with the inventor, with the assignee, or with anyone to whom there is an obligation to assign the application (37 C.F.R. 1.56 (c)) and who signs below.  
☐ [ ] the inventor(s) who signs below  
☐ [ ] the practitioner who signs below on the basis of the information:  
☐ [ ] supplied by the inventors  
☐ [ ] supplied by an individual designated in § 1.56(c)  
☐ [ ] in the practitioners file

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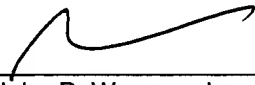
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Date: 2/16/01

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Reg. No. 35,398



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CDST-C130.CIP

Inventor(s): Lee et al.

Serial No.: 09/588,115

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Filed: May 30, 2000

Examiner: Not yet assigned

Title: MULTILAYER ELECTRODE STRUCTURE AND METHOD FOR FORMING MULTILAYER ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY DEVICE

The Commissioner of Patents and Trademarks  
Washington, D.C. 20231  
Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,821,622	LIQUID CRYSTAL DISPLAY DEVICE	10/13/98
4,697,123	GAS DISCHARGE PANEL	9/29/87
3,803,443	CHARGED PARTICLE BEAM SCANNING DEVICE WITH ELECTROSTATIC CONTROL	4/9/74

**Foreign Patent or Published Foreign Patent Application**

EP 503638 A2	ARRAY OF FIELD EMISSION CATHODES	9/16/92
EP 542271 A2	THIN-FILM DEVICE WITH A COMPOUND LAYER	5/19/93
WO 98/43268	FIELD EMITTER CATHODE BACKPLATE STRUCTURES FOR DISPLAY PANELS	10/1/98
EP 964423 A1	GRID ELECTRODES FOR A DISPLAY DEVICE	12/15/99
EP 855451 A1	WIRING FILM, SPUTTER TARGET FOR FORMING THE WIRING FILM AND ELECTRONIC COMPONENT USING THE SAME	7/29/98

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EP 731507 A1	ELECTRODE MATERIALS	9/11/96
DE 3720298 A1	METALLSCHICHTANORDNUNG FUR DUNNSCHICHTHYBRIDSCHALTUNGEN	12/29/88
WO 98/49705	INTEGRATED METALLIZATION FOR DISPLAYS	11/5/98
WO 93/21650	SELF SUPPORTING FLAT VIDEO DISPLAY	10/28/93

#### Other Documents

Patent Abstracts of Japan Vol. 017, no. 518 (E-1434), 17 September 1993 (1993-09-17) & JP 05 136411 A (Casio Comput Co Ltd), 1 June 1993 (1993-06-01) abstract

Patent Abstracts of Japan Vol. 011, no. 003 (C-395), 7 January 1987 (1987-01-07) & JP 61 183433 A (Matsushita Electric Ind Co Ltd), 16 August 1986 (1986-08-16) abstract

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